

ISim (0.76xd) - [Default.wcfg*]

File Edit View Simulation Window Layout Help

1.00us Re-launch

Instances and Processes

Objects

Simulation Objects for test_lcd_drv0

Name	Value
reset_b	1
sysclk	0
lcd_en	U
lcd_rs	U
lcd_rw	U
db7	U
db6	U
db5	U
db4	U
sysclk_period	10000 ps
count100[6:0]	93
en1u	0

1,130.533 ns

1,100 ns 1,150 ns 1,200 ns 1,250 ns 1,300 ns 1,350 ns

10000 ps

89 90 91 92 93 94 95 96 97 98 99 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18

X1: 1,130.533 ns

Default.wcfg*

Console

```
#restart
ISim>
# run 1.00us
Simulator is doing circuit initialization process.
Finished circuit initialization process.
ISim>
# run 1.00us
ISim>
```

STEP 0

Console Compilation Log Breakpoints Find in Files Results Search Results

Sim Time: 2,000,000 ps

Windows taskbar showing icons for Internet Explorer, Google Chrome, and ISim. System tray shows the date 11:59 平成 23/11/18.

ISim (0.76xd) - [Default.wcfg*]

File Edit View Simulation Window Layout Help

10ms Re-launch

Instances and Processes Objects

Simulation Objects for test_lcd_drv0

Object Name	Value
reset_b	1
sysclk	0
lcd_en	0
lcd_rs	0
lcd_rw	0
db7	0
db6	0
db5	0
db4	0
sysclk_period	10000 ps
count100[6:0]	1010000
en1u	0
count2000[1:0]	1999
en2m	1
en2mp	0
countprog[5:0]	000000

2,000,000,000 ns

1,999,000 ns 1,999,500 ns 2,000,000 ns 2,000,500 ns 2,001,000 ns 2,001,500 ns

10000 ps

1999 1999 0 1

000000 000001

X1: 2,000,000,000 ns

Default.wcfg* lcd_drv0.vhd

Console

```
# run 10ms
Simulator is doing circuit initialization process.
Finished circuit initialization process.
ISim>
# run 10ms
ISim>
```

STEP 1

Console Compilation Log Breakpoints Find in Files Results Search Results

Uses unsigned decimal representation Sim Time: 20,000,000,000 ps

12:32 平成 23/11/18

ISim (0.76xd) - [Default.wcfg*]

File Edit View Simulation Window Layout Help

10ms Re-launch

Instances and Processes Objects

Simulation Objects for uut

Instance and Process Name	Object Name	Value
test_lcd_drv0	reset_b	1
U0	sysclk	0
:63	lcd_en	U
:76	lcd_rs	U
:85	lcd_rw	U
:100	db7	U
:108	db6	U
:111	db5	U
:sysclk_process	db4	U
:stim_proc	count100[6...	1010000
std_logic_1164	en1u	0
std_logic_arith	count2000[...	000000000000
std_logic_unsigned	en2m	0
	en2mp	0
	countprog[...	001010
	rom_data[...	000000000000

Name	Value
reset_b	1
sysclk	0
lcd_en	U
lcd_rs	U
lcd_rw	U
db7	U
db6	U
db5	U
db4	U
sysclk_period	10000 ps
countprog[5]	8
rom_data[11]	000000000000

Time (ms)	countprog	rom_data
3	3	111000100...
4	4	111000001100
5	5	1110000000001
6	6	111000000110
7	7	111101000001
8	8	000000000000
9	9	000000000000

Default.wcfg*

STEP 2

Console

```

Finished circuit initialization process.
ISim>
# run 10ms
ISim>
# run 10ms
ISim>
  
```

Console Compilation Log Breakpoints Find in Files Results Search Results

Sim Time: 20,001,000,000 ps

ISim (0.76xd) - [Default.wcfg*]

File Edit View Simulation Window Layout Help

10ms Re-launch

Instances and Processes Objects

Simulation Objects for test_lcd_drv0

Object Name	Value
reset_b	1
sysclk	0
lcd_en	U
lcd_rs	U
lcd_rw	U
db7	U
db6	U
db5	U
db4	U
sysclk_period	10000 ps
countprog[5]	8
rom_data[11]	000000000000
next_sig	0

X1: 25.916666667 ms

STEP3

Console

```

Finished circuit initialization process.
ISim>
# run 10ms
ISim>
# run 10ms
ISim>
  
```

Console Compilation Log Breakpoints Find in Files Results Search Results

Uses unsigned decimal representation Sim Time: 30,000,000,000 ps

ISim (0.76xd) - [Default.wcfg*]

File Edit View Simulation Window Layout Help

20ms Re-launch

Instances and Processes Objects

Simulation Objects for U1

Instance and Process Name	Object Name	Value
test_lcd_drv0	reset_b	1
└─ uut	sysclk	0
└─ U0	en2mp	0
└─ U1	en1u	0
└─ :79	rom_data[...]	00000000000000
└─ :92	lcd_en	0
└─ :101	lcd_rs	0
└─ :116	lcd_rw	0
└─ :124	db7	0
└─ :127	db6	1
└─ :141	db5	0
└─ :sysclk_process	db4	0
└─ :stim_proc	go	0
└─ std_logic_1164	cyc	0
└─ std_logic_arith	keep_data[...]	00000000000000
└─ std_logic_unsigned	intcount[2:0]	000
	upperlower	0

Name	Value
reset_b	1
sysclk	1
lcd_en	1
lcd_rs	1
lcd_rw	0
db7	0
db6	1
db5	0
db4	0
sysclk_period	10000 ps
rom_add[5:0]	8
rom_data[11]	000000000000

16,001.195000 us

16,000 us 16,005 us 16,010 us 16,015 us 16,020 us 16,025 us

10000 ps

7 8

1111101000001 0000000000000

X1: 16,001.195000 us

Default.wcfg*

Console

at 0 ps, Instance /test_lcd_drv0/uut/U0/: Warning
 at 0 ps, Instance /test_lcd_drv0/uut/U0/: Warning
 Finished circuit initialization process.
 ISim>
 # run 20ms
 ISim>

Console Compilation Log Breakpoints Find in Files Results Search Results

STEP4

Sim Time: 20,001,000,000 ps

Windows taskbar showing icons for Internet Explorer, Google Chrome, and other applications. System tray shows the time 23:48 and date 平成 23/11/18.